

L Number	Hits	Search Text	DB	Time stamp
1	42177	master same slave	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:17
2	26321	system adj clock	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:17
3	3116	transmit adj clock	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:17
4	71	(transmit adj clock) near3 master	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:17
5	35385	phase near2 adjust\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:35
6	58	(transmit adj clock) same (phase near2 adjust\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:19
8	1	(master same slave) and ((system adj clock) and ((transmit adj clock) same (phase near2 adjust\$4)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:20
7	10	(system adj clock) and ((transmit adj clock) same (phase near2 adjust\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:25
9	419	(master same slave) and (transmit adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:25
10	15	(master same slave) and ((transmit adj clock) same (phase near2 adjust\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:26
11	14	((master same slave) and ((transmit adj clock) same (phase near2 adjust\$4))) not ((system adj clock) and ((transmit adj clock) same (phase near2 adjust\$4)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:29
12	4775	(phase near2 adjust\$4) same clock	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:31
13	294	((phase near2 adjust\$4) same clock) and (master same slave)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:31
14	9	(system adj clock) and (transmit adj clock) and ((phase near2 adjust\$4) same clock) and (master same slave)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:31
15	76	phase near2 adjust\$4 near3 arbitrar\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:36
16	0	(master same slave) and (phase near2 adjust\$4 near3 arbitrar\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:36
17	428	(phase near2 adjust\$4) same arbitrar\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:36

18	13	(master same slave) and ((phase near2 adjust\$4) same arbitrar\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:45
19	9	(phase adj shift\$4) near3 (transmit adj clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:06
20	3289	(phase adj shift\$4) near3 clock	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:50
21	232	(master same slave) and ((phase adj shift\$4) near3 clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 14:51
22	25	(transmit adj clock) and ((master same slave) and ((phase adj shift\$4) near3 clock))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:04
23	6613	dll (delay adj lock\$4 adj loop)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:05
24	9297	feedback near3 phase	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:05
25	122	(dll (delay adj lock\$4 adj loop)) same (feedback near3 phase)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:05
26	112556	(phase adj shift\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:24
27	9	((dll (delay adj lock\$4 adj loop)) same (feedback near3 phase)) same ((phase adj shift\$4) )	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:56
28	6658	(phase adj shift\$4) same ("90" adj degrees)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:53
29	15	(dll (delay adj lock\$4 adj loop)) and (feedback near3 phase) and ((phase adj shift\$4) same ("90" adj degrees))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:47
30	310	(dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) )	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:48
31	20	(dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:48
32	5058	(phase adj shift\$4) near4 ("90")	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:55
33	0	((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) near4 ("90"))) not ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:55
34	310	((phase adj shift\$4) ) same (dll (delay adj lock\$4 adj loop))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:57

35	113	driver and (((phase adj shift\$4) ) same (dll (delay adj lock\$4 adj loop)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:58
36	309937	feedback	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:59
37	73	(driver and (((phase adj shift\$4) ) same (dll (delay adj lock\$4 adj loop)))) and feedback	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:59
38	64	((driver and (((phase adj shift\$4) ) same (dll (delay adj lock\$4 adj loop)))) and feedback) not ((dll (delay adj lock\$4 adj loop)) same ((phase adj shift\$4) same ("90" adj degrees)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/03/29 15:59